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## APPLICANTS

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Shih-Chang Chen, Hsin-Chu, TAIWAN;\*\* CONTINUING DATA \*\*\*\*\* *NONE**CME*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *NONE**CME*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met				
Verified and Acknowledged <i>Allowance</i> <i>Chen</i> Examiner's Signature Initials				

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## TITLE

Semiconductor wafer manufacturing methods employing cleaning delay period

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